PATENT

TESSERA 3.3-018 CONT CONT II DIV CONT II

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of Khandros et al.

Group Art Unit:

Application No.: Unassigned

Examiner:

Filed: On Even Date Herewith

Date: October 19, 2000

For: STACKED CHIP ASSEMBLY

BOX PATENT APPLICATION
Assistant Commissioner For Patents

Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir or Madam:

Preliminary to the initiation of the prosecution of the above-identified pending U.S. patent application, the following amendments and remarks are respectfully submitted.

IN THE TITLE:

Please amend the title of the application to: --Stacked Chip Assembly --.

## IN THE SPECIFICATION:

Please insert before the first line, after the title, the following:

CROSS-REFERENCE TO RELATED APPLICATIONS

This is a continuation of United States Patent Application No. 09/122,219, filed July 24, 1998, which is in turn a continuation of United States Patent Application No. 08/861,280, filed on May 21, 1997, which is in turn a continuation of United States Patent Application No. 08/319,966, filed on October 7, 1994, now United States Patent No. 5,685,885. United States Patent Application No. 08/319,966 is a continuation of United States Patent Application No. 08/319,966 is a continuation of United States Patent Application No. 08/030,194, filed April 28, 1993 as the national phase of International Application PCT/US91/06920 filed September 24, 1991. Said 08/030,194 application, now

EXPRESS MAIL LABEL NUMBER: EL408439244US